

Figure 1

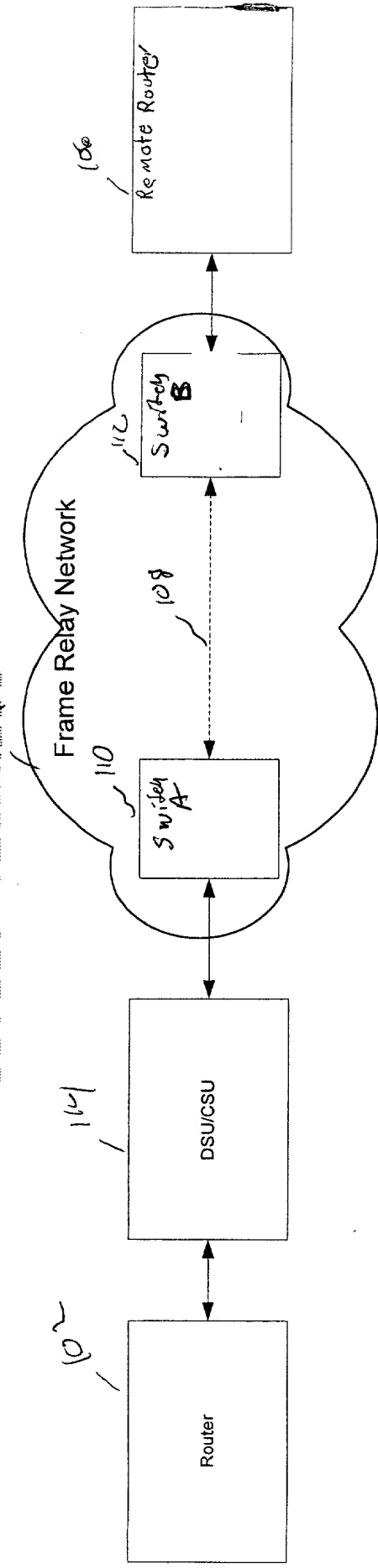


Fig. 2

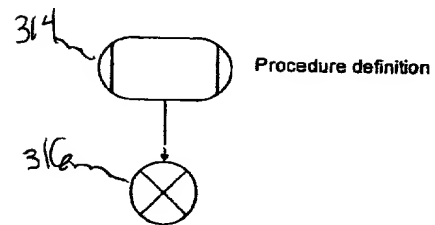
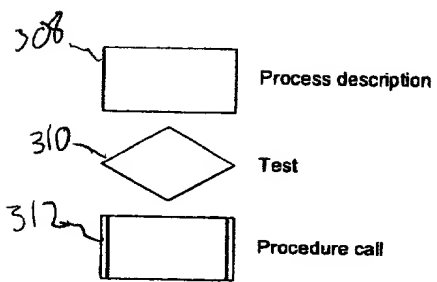
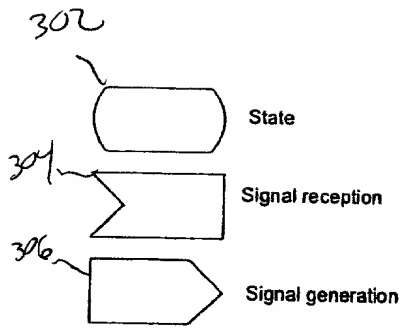


Fig. 3

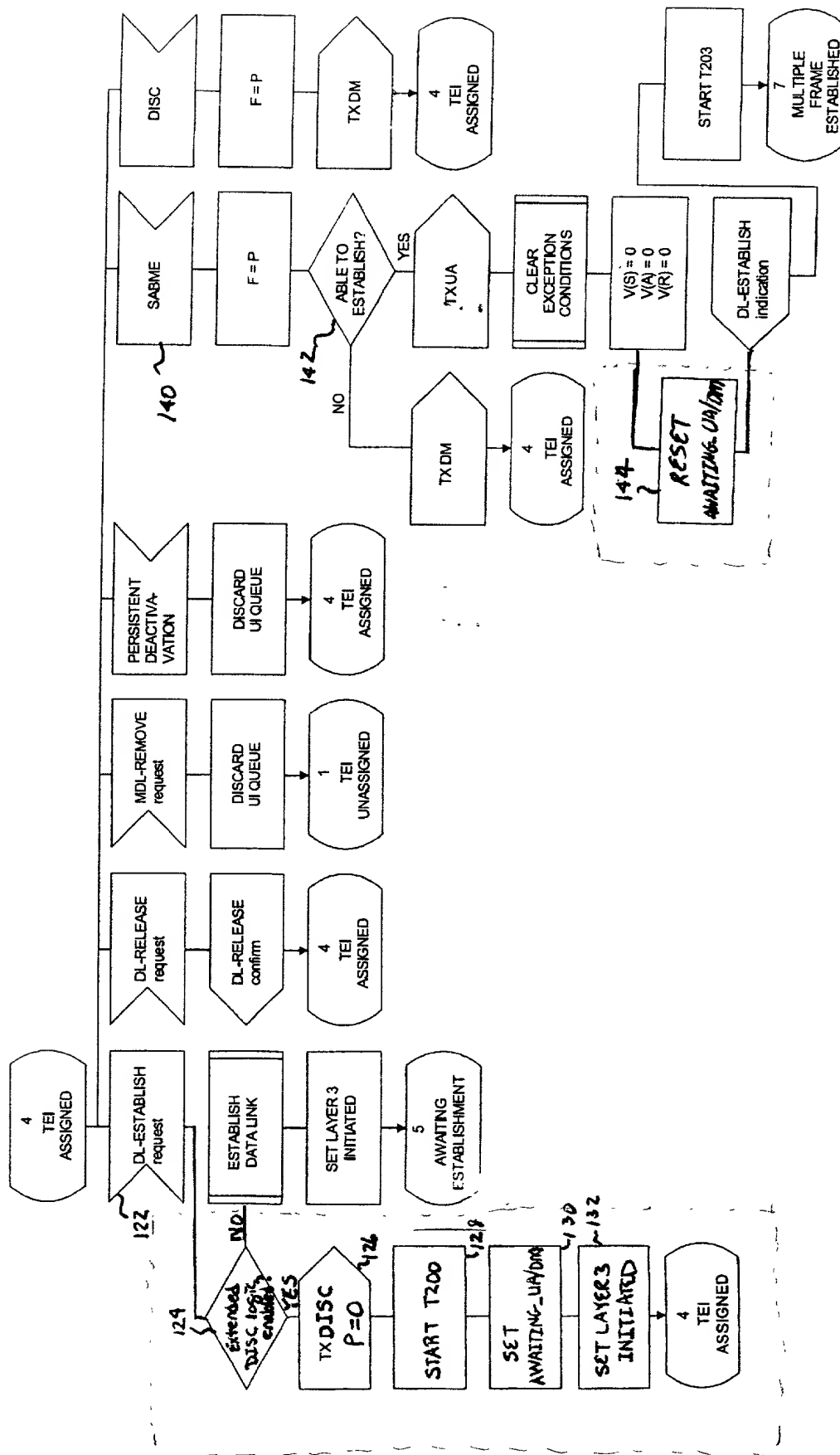


Fig. 4A

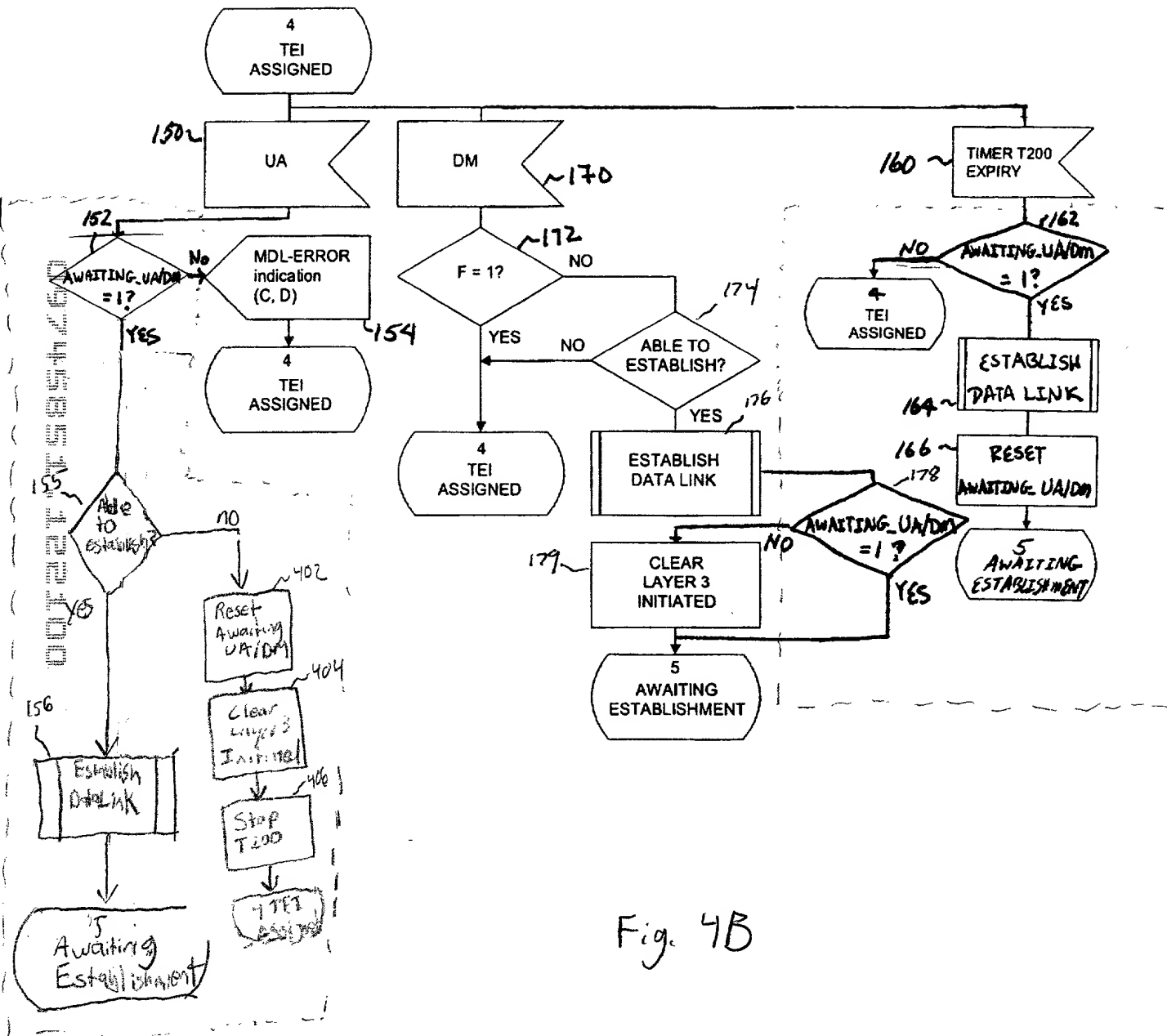


Fig. 4B

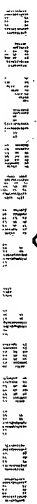


Fig. 5A

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graph TD
    Start([5 AWAITING ESTABLISHMENT]) --> Split(( ))
    Split --> Disc[/DISC/]
    Split --> UA[/UA/]
    
    Disc --> F_P[F = P]
    F_P --> P1{P = 1?}
    P1 -- YES --> TX_DM[/TX DM/]
    TX_DM --> Wait5([5 AWAITING ESTABLISHMENT])
    P1 -- NO --> ResetAndRelease[RESET AND RELEASE]
    ResetAndRelease --> VSA[V(S) = V(A) + 1]
    VSA --> UA
    
    UA --> F1{F = 1?}
    F1 -- NO --> MDLError[/MDL-ERROR indication (D)/]
    MDLError --> Wait5_2([5 AWAITING ESTABLISHMENT])
    F1 -- YES --> Layer3{LAYER 3 INITIATED?}
    Layer3 -- NO --> VSA_2{V(S) = V(A)?}
    VSA_2 -- YES --> DL_Establish_Confirm[/DL-ESTABLISH confirm/]
    VSA_2 -- NO --> DiscardI[DISCARD I QUEUE]
    DiscardI --> DL_Establish_Indication[/DL-ESTABLISH indication/]
    DL_Establish_Confirm --> StopT200StartT203[STOP T200  
START T203]
    DL_Establish_Indication --> StopT200StartT203
    
    StopT200StartT203 --> VSA_3[V(S) = 0  
V(A) = 0  
V(R) = 0]
    VSA_3 --> ResetAwaitingUA[RESET AWAITING UA/DM]
    ResetAwaitingUA --> MultipleFrame([7 MULTIPLE FRAME ESTABLISHED])
    
    %% Side path from ResetAndRelease
    ResetAndRelease --> DiscardIUI[DISCARD I AND UI QUEUES]
    DiscardIUI --> DLRelease[/DL-RELEASE indication/]
    DLRelease --> StopT200StopT203[STOP T200  
STOP T203]
    StopT200StopT203 --> End((X))
  
```

Fig. 5B

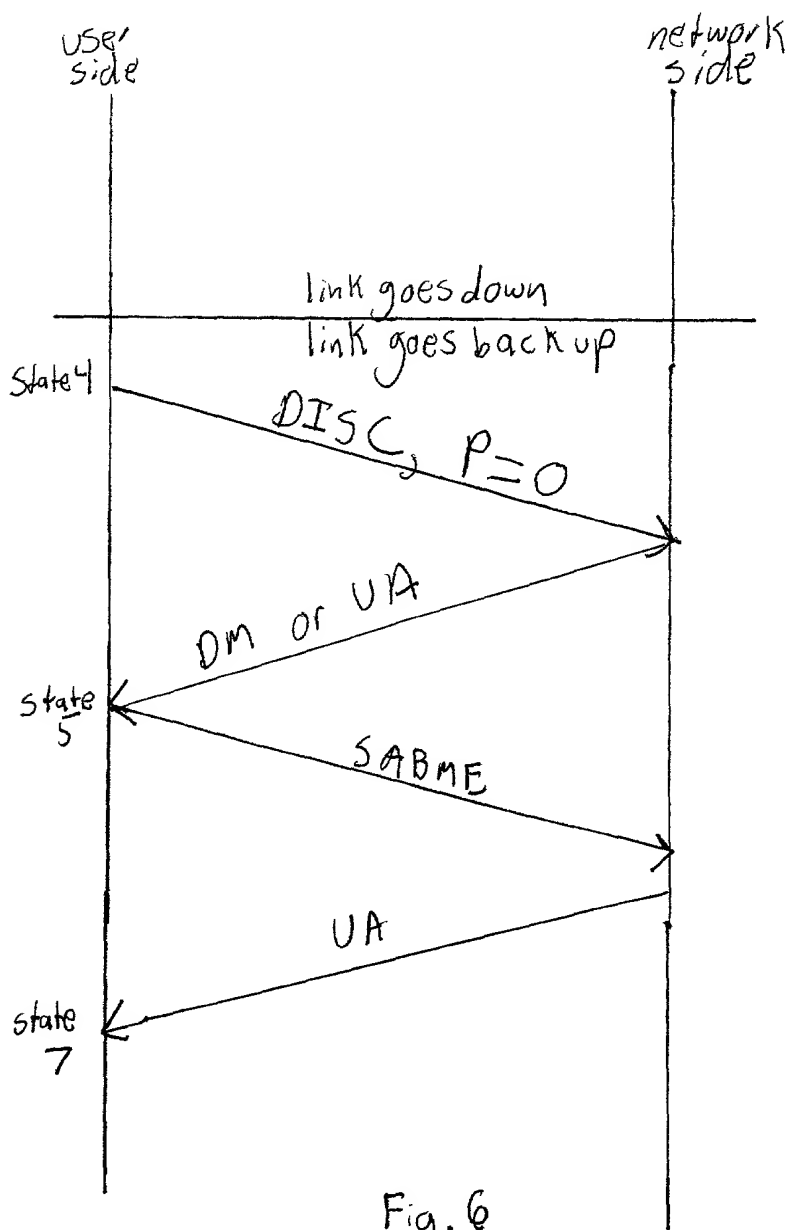
[illegible]

Fig. 6